**North SouthUniversity**

Department of Computer Science and Engineering

Quiz-6, Section – 4, Fall-2016

Course No: **CSE 231** Course Title: **Digital Logic Design**

Time: 20 min Full Marks : 10

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| 1. | Consider a sequential circuit with two D flip-flops A and B, two inputs x and y, and one output z. The flip-flop input equations and the circuit output are as follow :  A(t+1) = x’y + xA  B(t+1) = x’B + xA  z = B  a. Draw the logic diagram of the circuit  b. Tabulate the state table  c. Draw state diagram | 10 |
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